



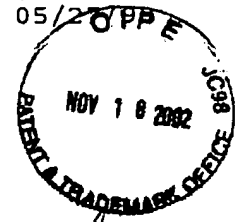
CARD NO: 17858

U.S. Patent Application Docket No: 990559  
Serial No: 09/320,271 Filed: 05/20/02  
Patent Number: Issued:  
Applicant(s): WATANABE, et al.

Papers filed herewith on: 11/18/02

Fees: \$ 740.00

Other: RCE AND IDS: PTO-1449 w/ References <sup>and</sup> Related Copending Applications



COMMISSIONER OF PATENTS

Receipt is hereby acknowledged of the papers filed as indicated  
in connection with the above-identified case.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of: **WATANABE, et al.**

Serial No.: **09/320,271**

Group Art Unit: **2825**

Filed: **May 27, 1999**

Examiner: **Calvin Lee**

**P.T.O. Confirmation No.: 4409**

For: **SEMICONDUCTOR DEVICE AND FABRICATION METHOD THEREOF**

**REQUEST FOR CORRECTION OF PATENT OFFICE RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 25, 2004

Sir:

Applicants received a Notice of Allowability dated February 18, 2004, indicating that claims 1-13, 20 and 21 have been allowed. However, the allowed claims should be 1-13, 21 and 22. The Patent and Trademark Office is requested to correct the record.

In the event that this paper is not timely filed, Applicant respectfully petitions for an appropriate extension of time. Please charge any fees for such an extension of time and any other fees which may be due with respect to this paper, to Deposit Account No. 50-2866.

Respectfully submitted,  
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Attorney Docket No. **990559**

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